

1/16

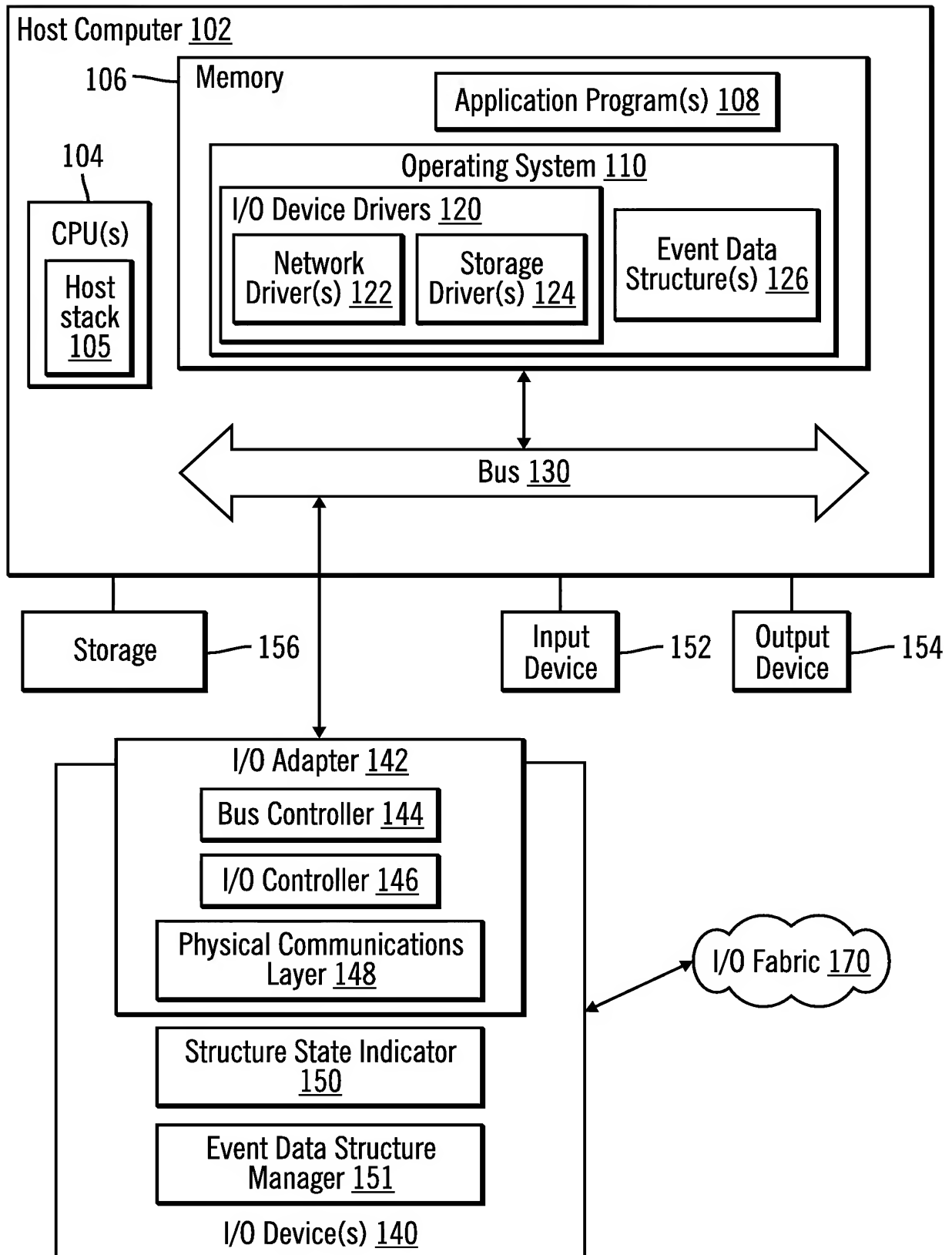


FIG. 1

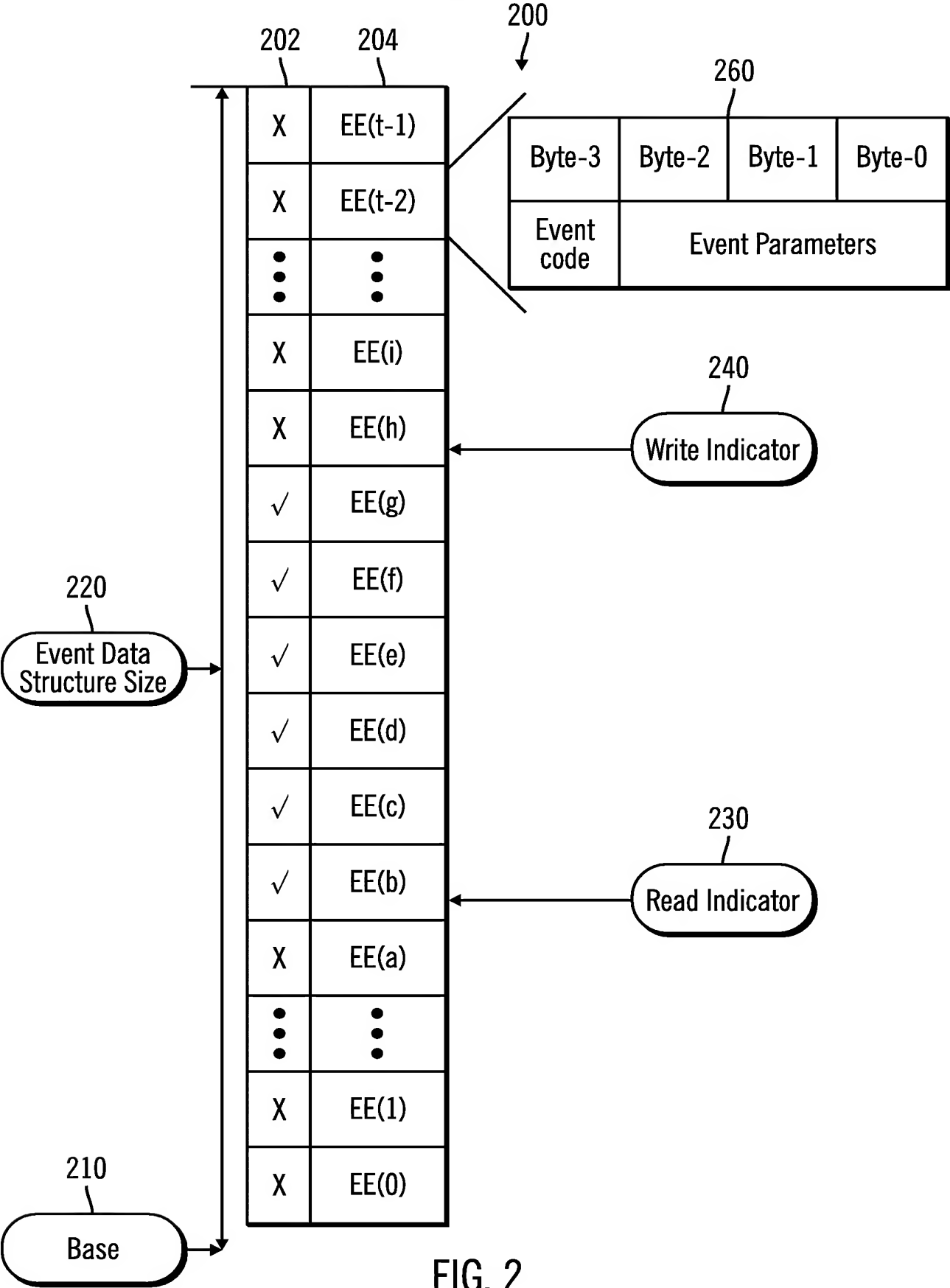


FIG. 2



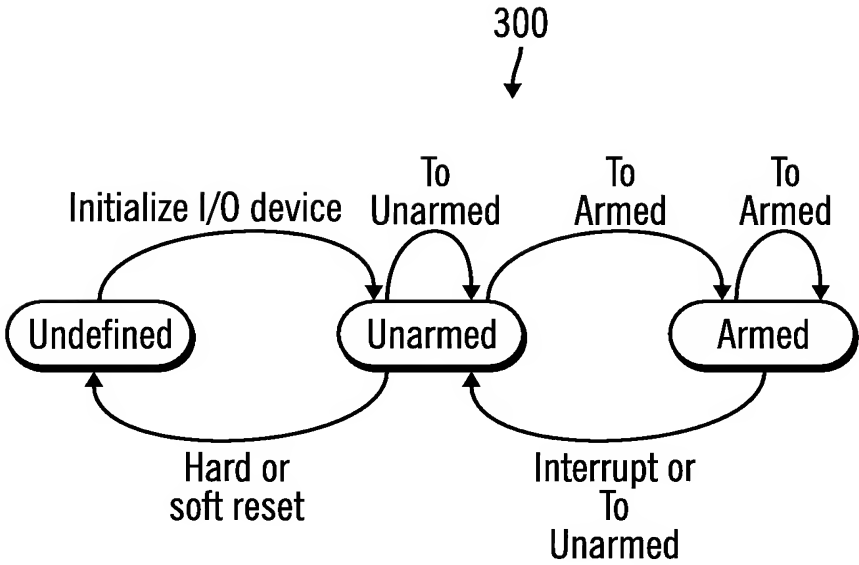


FIG. 3



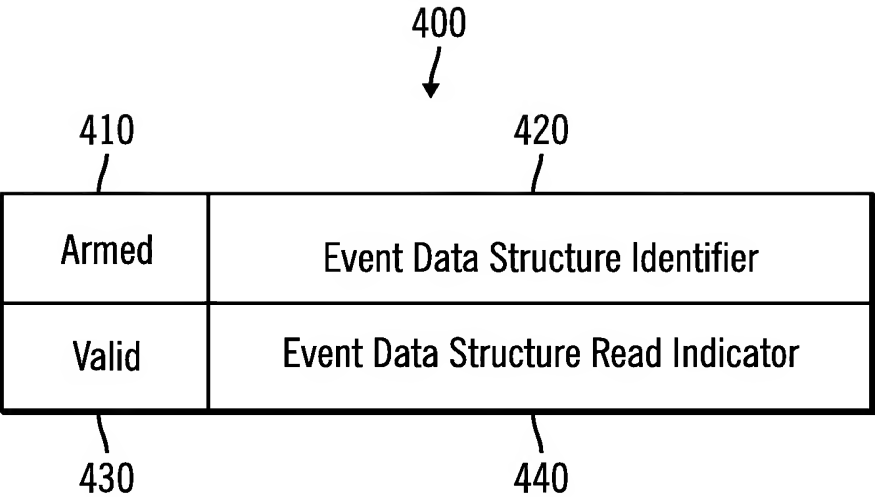


FIG. 4



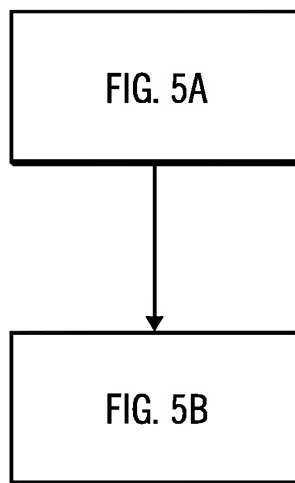


FIG. 5

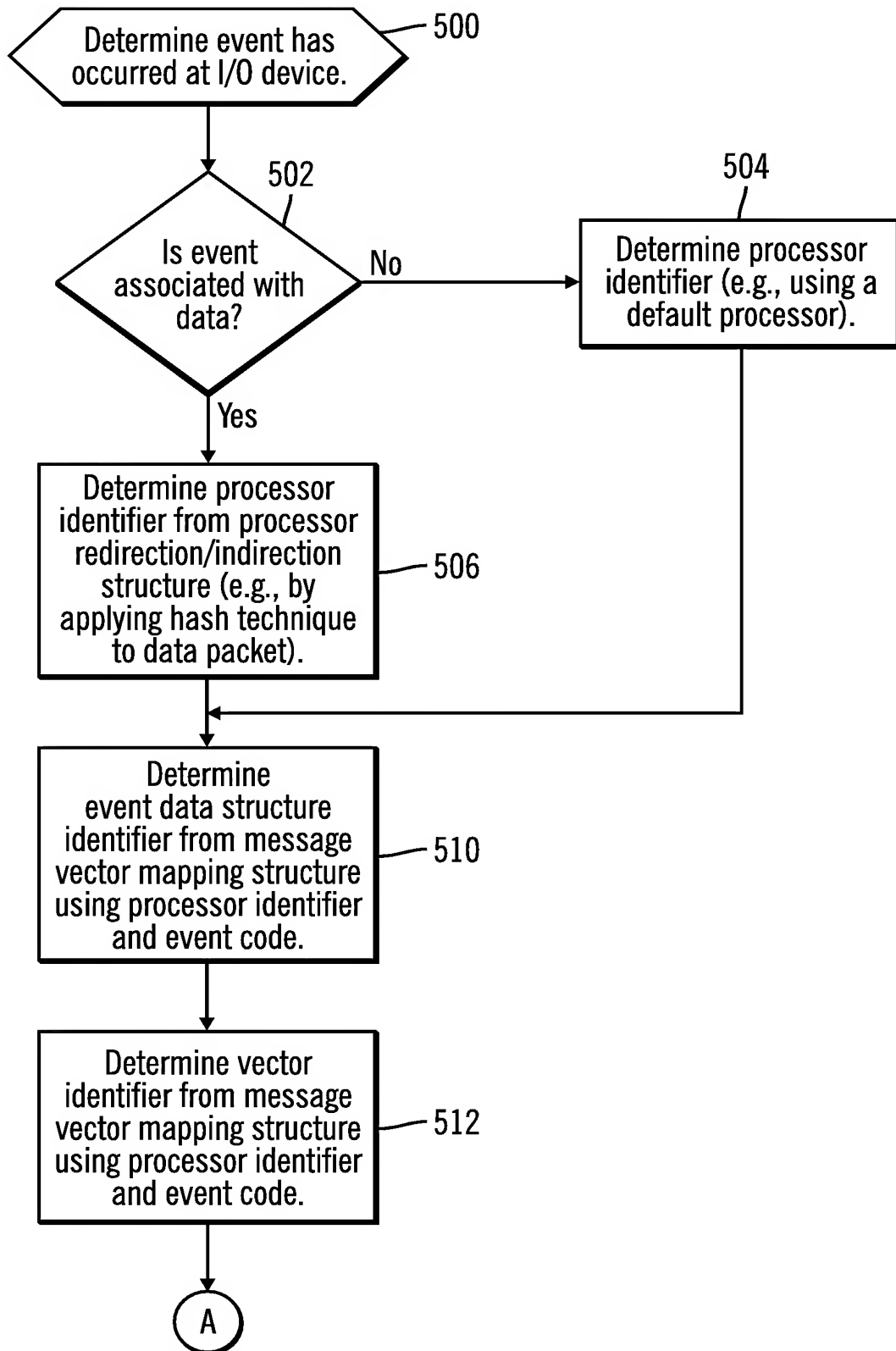


FIG. 5A



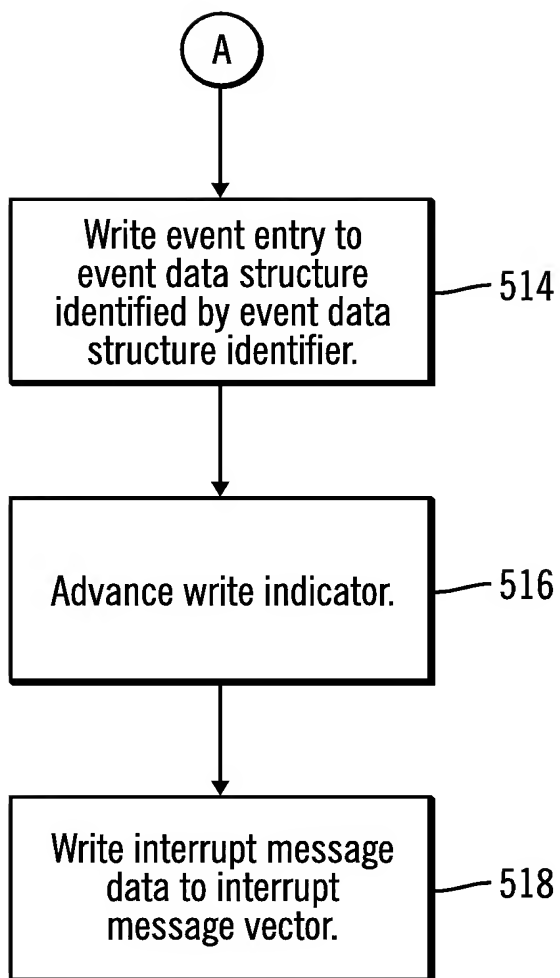


FIG. 5B

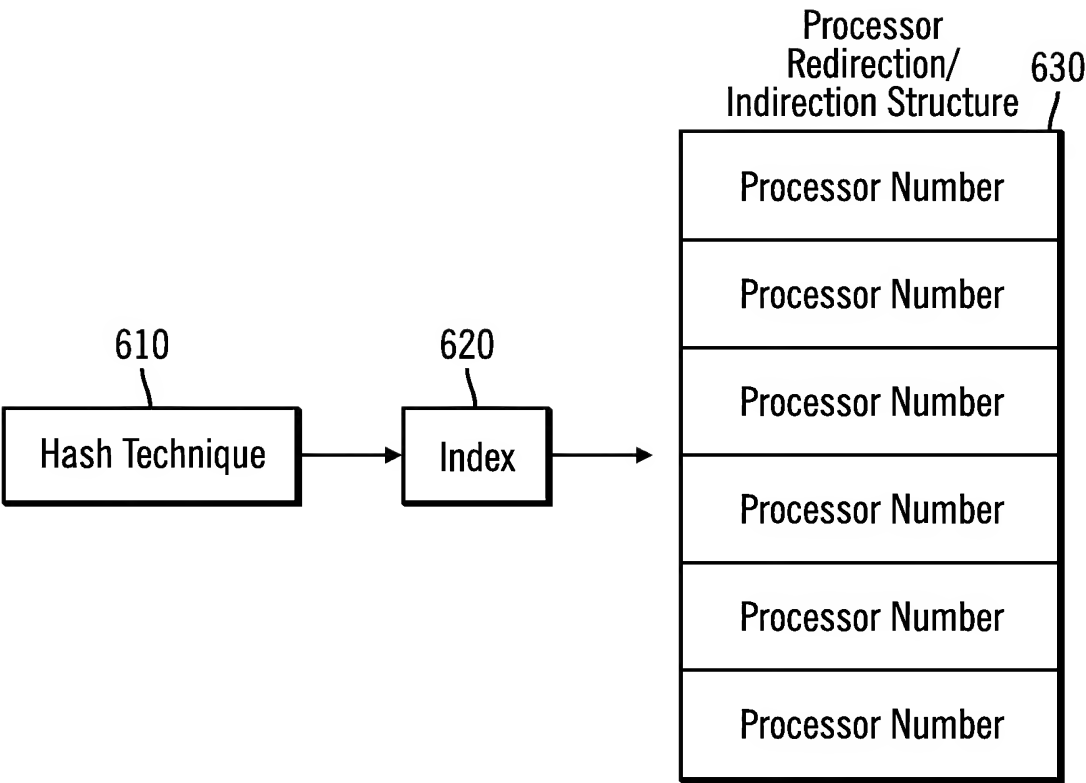


FIG. 6





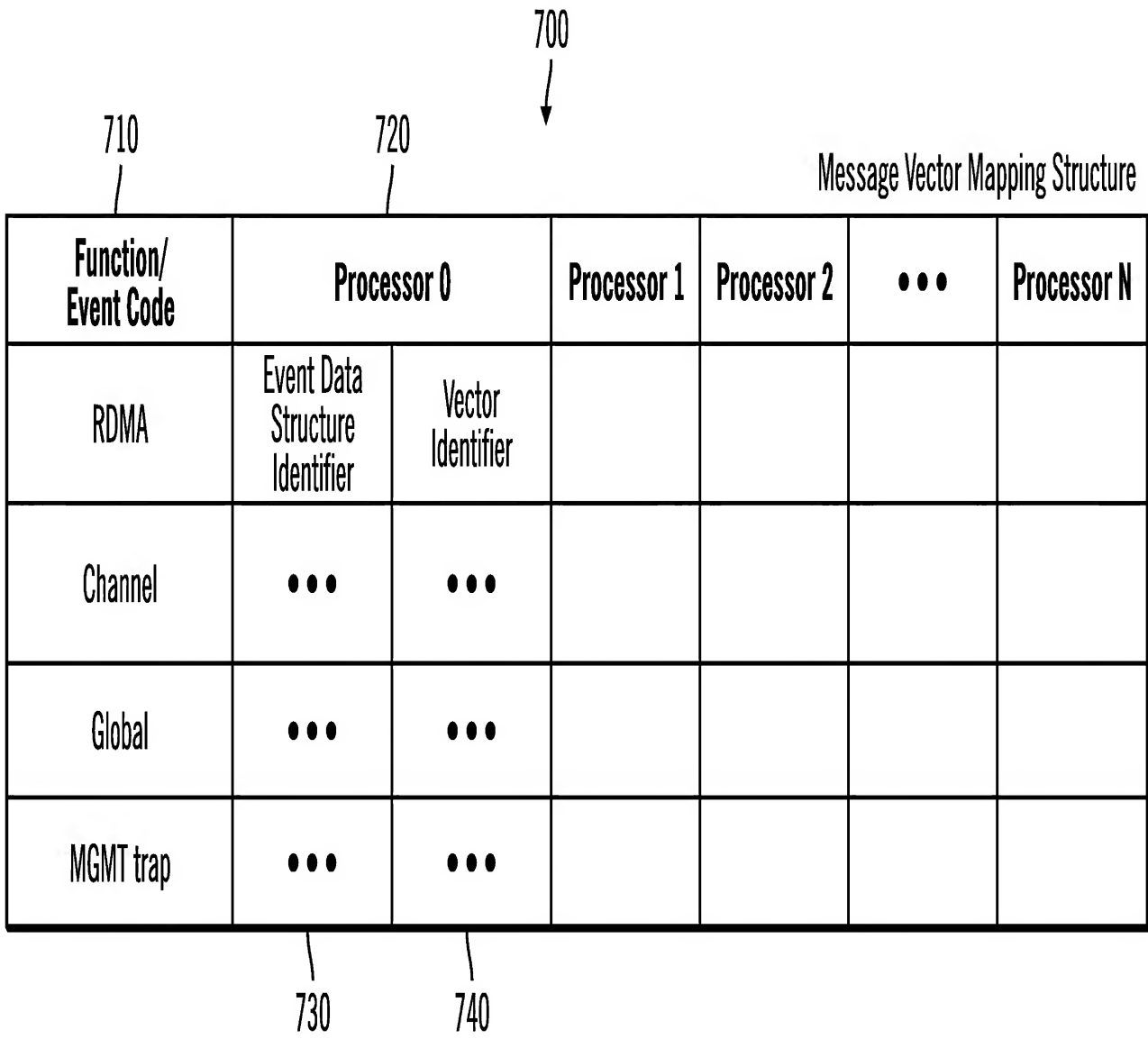


FIG. 7

850  
↓

860  
|

870  
|

Message Vector Mapping Structure

Function/ Event Code	Processor 0		Processor 1	Processor 2	...	Processor N
RDMA TX	Event Data Structure Identifier	Vector Identifier				
RDMA RX	Event Data Structure Identifier	Vector Identifier				
Channel TX	...	...				
Channel RX	...	...				
Global - General Purpose I/O (GPIO) Interrupts	...	...				
Global - Errors	...	...				
MGMT trap	...	...				

880  
|

890  
|

FIG. 8

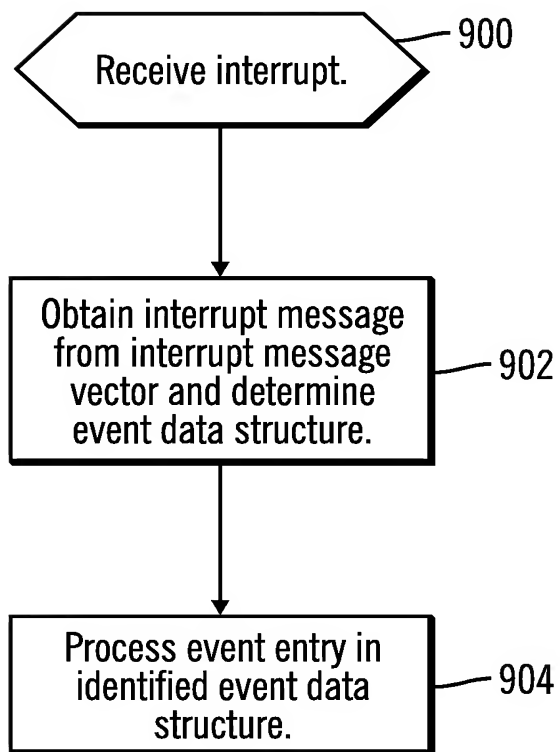


FIG. 9

12/16

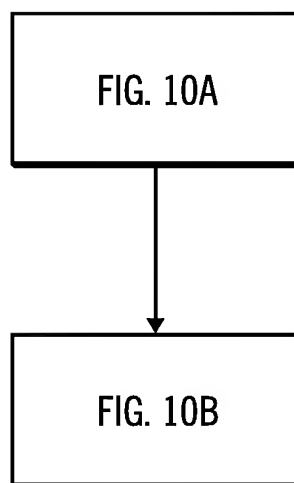


FIG. 10

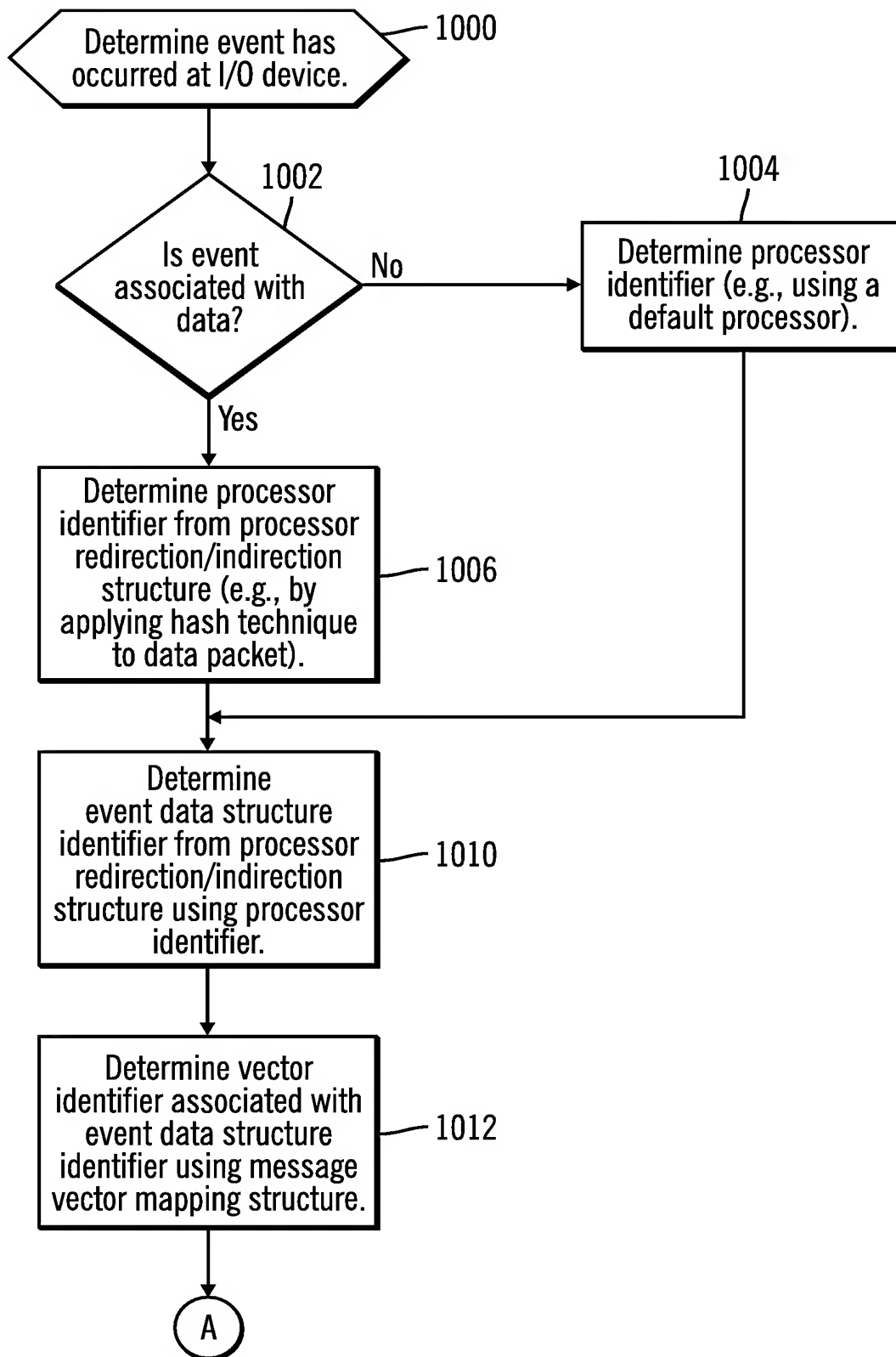


FIG. 10A



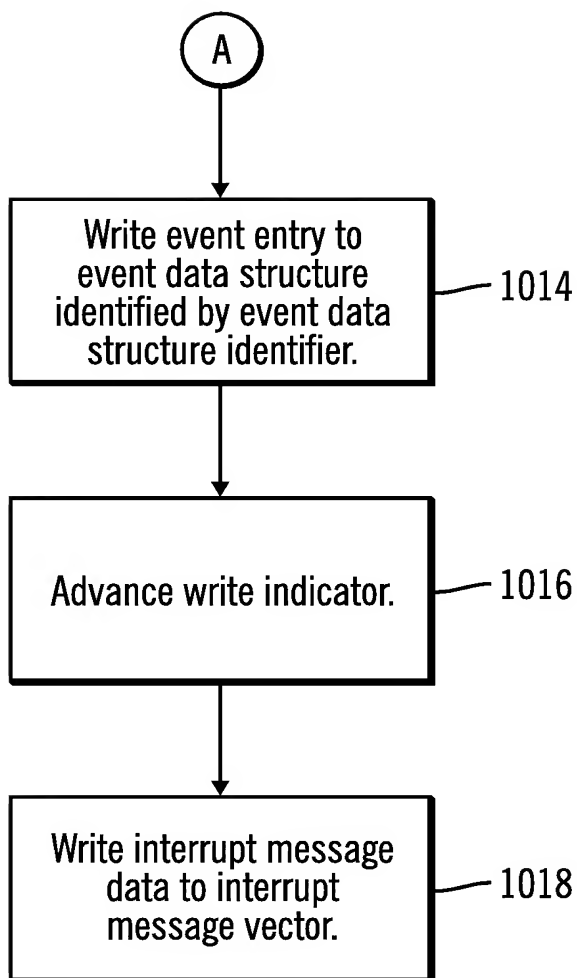


FIG. 10B

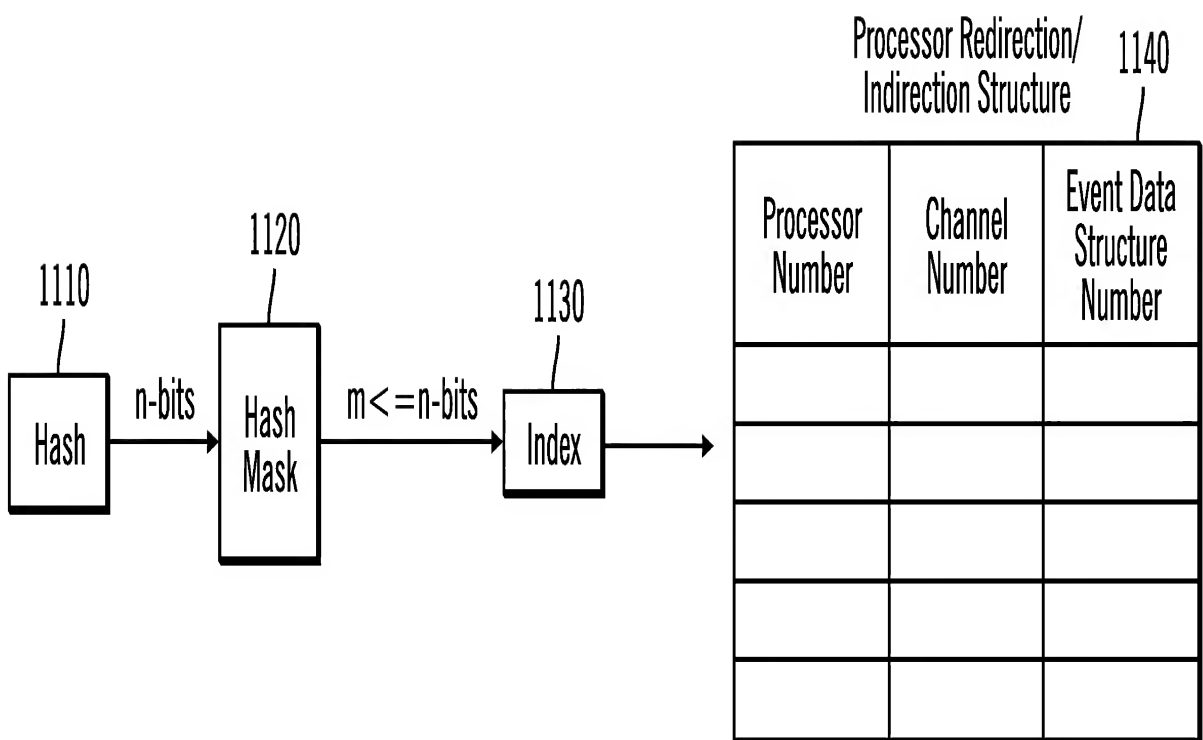


FIG. 11

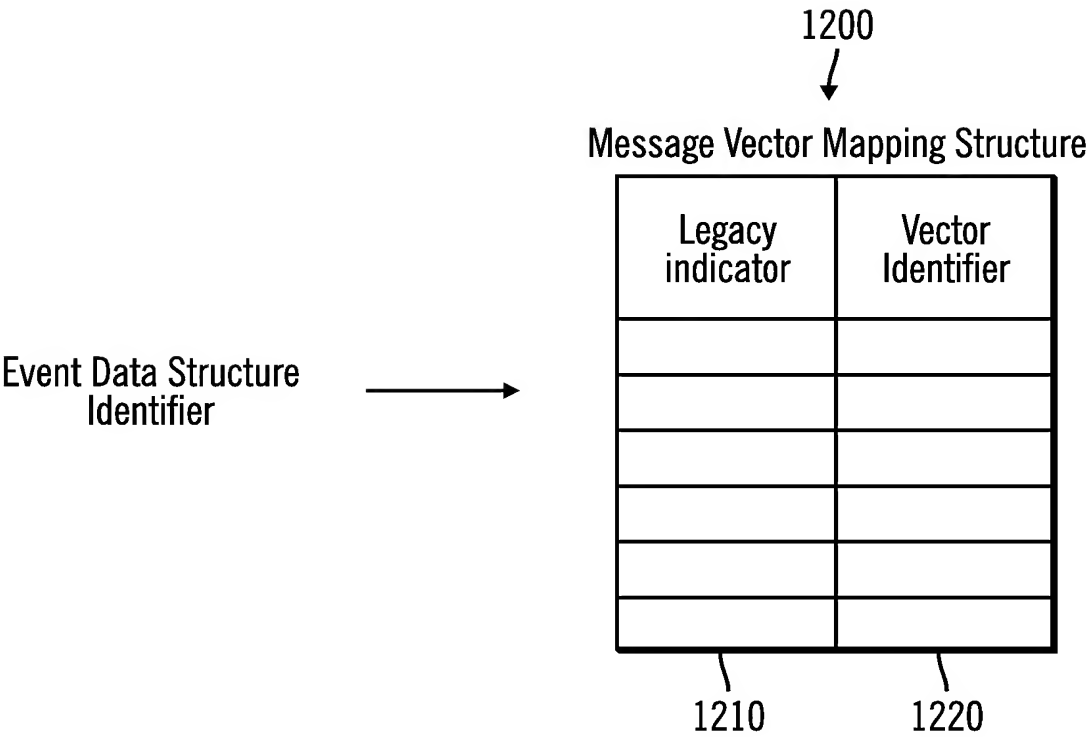


FIG. 12

